



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: G. DIWAKAR VISHAKHADATTA ET AL.

Filed: MARCH 29, 2001

For: DIGITAL INTERFACE IN RADIO-FREQUENCY APPARATUS AND ASSOCIATED METHODS

Serial No.: 09/821,340

Group Art Unit: 2631

Examiner: UNKNOWN

Atty Dkt: SILA:073

Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in a first class, postage prepaid envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on the date below:

11-11-02 Marty Bower
Date Name

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the document(s) listed on attached Form PTO-1449 be considered by the Examiner and made of record.

In accordance with 37 C.F.R. §§ 1.97(g),(h), this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

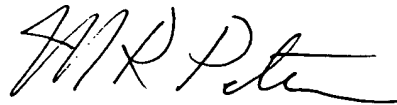
The present Supplemental Information Disclosure Statement is being filed prior to the receipt of a first Official Action reflecting an examination on the merits, and hence is believed to be

timely filed in accordance with 37 C.F.R. § 1.97(b). No fees are believed to be due in connection with the filing of this Supplemental Information Disclosure Statement, however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to these materials, the Commissioner is hereby authorized to deduct said fees from Deposit Account No. 10-1205/SILA:073.

A copy of the listed document(s) required by 37 C.F.R. § 1.98(a)(2) are enclosed for the convenience of the Examiner.

Applicant respectfully requests that the listed document(s) be made of record in the present case.

Respectfully submitted,

A handwritten signature in dark ink, appearing to read 'MR Peterson', written over a horizontal line.

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Enclosures

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List of Patents and Publications for Applicant's

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Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
	A5	6,148,048	11/14/00	Kerth et al.			9/26/97
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	C22	Analog Devices, AD7002 Specification, LC2MOS, GSM Baseband I/O Port, Rev. B, 1997, pp. 1-16.
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	C24	Kwentus et al., "A Single-Chip Universal Digital Satellite Receiver With 480-MHz IF Input," IEEE Journal of Solid-State Circuits, Vol. 34, No. 11, November 1999, pp. 1634-1646.
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	C35	Steyaert et al., "A 2-V CMOS Cellular Transceiver Front-End," IEEE Journal of Solid-State Circuits, Vol. 35, No. 12, December 2000, pp. 1895-1907.
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	C39	RF Micro Devices, RF2968, Product Description, Blue Tooth Transceiver, Rev A19, pp. 11-199-11-222.
	C40	Texas Instruments, TRF6901, "Single Chip RF Transceiver," March 2002, pp. 1-29.
	C41	Texas Instruments, TRF6900A, "Single Chip RF Transceiver," September 2001, pp. 1-34.
	C42	Texas Instruments, TRF6900, "Single Chip RF Transceiver, October 1999, pp. 1-32.
	C43	Philips Semiconductor, "Bluetooth RF Transceiver," Data Sheet, UAA3558, December 21, 2000, pp. 1-5.
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	C54	Ericsson, "RF Transceiver Circuit For The Digital Enhanced Cordless Telecommunications (DECT) System," PBL40215, January 2001, pp. 1-22.
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	C66	Aziz et al., "Performance Of Complex Noise Transfer Functions In Bandpass And Multi Band Sigma Delta Systems," IEEE, 1995, pp., 641-644.
	C67	Jantzi, "A Fourth-Order Bandpass Sigma-Delta Modulator," IEEE Journal Of Solid-State Circuits, Vol. 28, No. 3, March 1993, pp. 282-291.
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	C70	Thurston et al., "Bandpass Implementation Of The Sigma-Delta A-D Conversion Technique," International Conference On Analogue To Digital And Digital To Analogue Conversion, September 1991, 7 pgs.
	C71	Rudell, et al., "Second Generation Multi-Standard Monolithic CMOS RF Transceiver," University of California, Berkeley, Slides 1 through 9 (June 1996)
	C72	Cho, et al., "Multi-Standard Monolithic CMOS RF Transceiver," University of California, Berkeley, Slides 1 through 26 (June 1996)
	C73	Copending U.S. Patent Application Serial No. 09/821,342, filed March 29, 2001, "Partitioned Radio-Frequency Apparatus And Associated Method" (SILA:072)
	C74	Copending U.S. Patent Application Serial No. 09/821,340, filed March 29, 2001, "Digital Interface In Radio-Frequency Apparatus And Associated Methods" (SILA:073)
	C75	Copending U.S. Patent Application Serial No. 10/075,094, filed February 13, 2002, "Radio-Frequency Communication Apparatus And Associated Methods" (SILA:074)
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	C77	Copending U.S. Patent Application Serial No. 10/075,122, filed February 12, 2002, "Digital Architecture For Radio-Frequency Apparatus And Associated Methods" (SILA:078)

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	C79	Copending U.S. Patent Application Serial No. 10/081,121, filed February 22, 2002, "Calibrated Low-Noise Current And Voltage References And Associated Methods" (Sila:095)
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